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**Deveirter** -31-80-1002<sup>ⓘ</sup> Scip ZTEJ SLEEHW TOH<sup>ⓘ</sup> ^<sup>ⓘ</sup> 72-90-6102<sup>ⓘ</sup> Deveirter<sup>ⓘ</sup> · 9-1002<sup>ⓘ</sup> yeri<sup>ⓘ</sup> rek<sup>ⓘ</sup> 72-90-6102<sup>ⓘ</sup> veirter<sup>ⓘ</sup> · 61-80-0002<sup>ⓘ</sup> Naz<sup>ⓘ</sup> 72-90-6102<sup>ⓘ</sup> msomac<sup>ⓘ</sup> sleehw<sup>ⓘ</sup> toh<sup>ⓘ</sup> ^<sup>ⓘ</sup> 0202<sup>ⓘ</sup> 82<sup>ⓘ</sup> yraurbef<sup>ⓘ</sup> Deveirter<sup>ⓘ</sup> FAFACDEepS<sup>ⓘ</sup> · 6102<sup>ⓘ</sup> 21<sup>ⓘ</sup> Rebmedc<sup>ⓘ</sup> 6102<sup>ⓘ</sup> 01<sup>ⓘ</sup> YLUJ<sup>ⓘ</sup> skurt<sup>ⓘ</sup> repus<sup>ⓘ</sup> muidates<sup>ⓘ</sup> · ttam<sup>ⓘ</sup> revid<sup>ⓘ</sup> kurrt<sup>ⓘ</sup> repus<sup>ⓘ</sup> eissua<sup>ⓘ</sup> 76102<sup>ⓘ</sup> 6<sup>ⓘ</sup> Listen<sup>ⓘ</sup> to<sup>ⓘ</sup> this<sup>ⓘ</sup> article<sup>ⓘ</sup> (34<sup>ⓘ</sup> minutes)<sup>ⓘ</sup> This<sup>ⓘ</sup> audio<sup>ⓘ</sup> file<sup>ⓘ</sup> À<sup>ⓘ</sup> was<sup>ⓘ</sup> created<sup>ⓘ</sup> by<sup>ⓘ</sup> a<sup>ⓘ</sup> review<sup>ⓘ</sup> of<sup>ⓘ</sup> article<sup>ⓘ</sup> dated<sup>ⓘ</sup> July<sup>ⓘ</sup> 1<sup>ⓘ</sup> 2018<sup>ⓘ</sup> -01<sup>ⓘ</sup> 2018<sup>ⓘ</sup> and<sup>ⓘ</sup> does<sup>ⓘ</sup> not<sup>ⓘ</sup> reflect<sup>ⓘ</sup> any<sup>ⓘ</sup> subsequent<sup>ⓘ</sup> changes<sup>ⓘ</sup>. (Audio help ÀÀ<sup>ⓘ</sup> Other<sup>ⓘ</sup> articles<sup>ⓘ</sup> spoken<sup>ⓘ</sup>) Official site Retrieved from À<sup>ⓘ</sup> 2<sup>ⓘ</sup>Computer architectures using an 8-bit word À8-bitÀ<sup>ⓘ</sup> redirects here. For other uses, see 8-bit (disambiguation). This article has multiple editions. Please help improve it or discuss these issues on the discussion page. (Learn how and when to remove these template messages) This article requires additional quotes for verification. Please help improve this article by adding quotes to reliable sources. Find sources: À8-bitcomputerÀ<sup>ⓘ</sup> ÀnewsÀÀ<sup>ⓘ</sup> newspapersÀÀ<sup>ⓘ</sup> booksÀÀ<sup>ⓘ</sup> scholarÀÀ<sup>ⓘ</sup> JSTOR<sup>ⓘ</sup> (October 2009) (Learn how and when to remove this message template) This article relies too much on references to primary sources. Please improve it by adding secondary or tertiary sources. (August 2012) (Discover how and when to remove this message template) This article contains original searches. Please improve it by checking the claims made and adding quotes online. Claims that consist solely of original research should be deleted. (May 2019) (See how and when to remove this message template) (See how and when to remove this message template) Computer architecture Bit width 1 248 121 618 242 628 303 132 364 045 486 064 128 256 512bit Slicing Application 8 163 264 Precision floating-point binary 16 (ÀÀÀ 1â2) 2432 (ÀÀ 1) 4064 (ÀrÀÀ2) 80 128 (ÀðÀÀ24) 256 (ÀâÀ88) Floating point decimal precision 3 264 128 vte Nellâ128 computer architecture, the 8-bit integers or other unitsÀexclusively data are the 8-bit (1-byte) ones. In addition, the unit architectures 8-bit central processing (CPU) and otilos otilos id ,tib 8 id idnary 'Aip etnemlareneg onos tib 8 a UPC ei rep jotnemazziridni id sub i idniug ef aïromem id izziridni iïG .inoisnemid elleuq id itad sub o irtsiger us etasab elleuq onos JULA( ehcitemtira ehcigol 8 -bit microcomputer are microcomputer that use 8 -bit microprocessors. The term '8-bit' is also applied to the sets of characters that could be used on computers with 8-bit bytes, the most well-known of which is made up of various forms of extended ascii, including the ISO/IEC 8859 series series of sets of national characters is À Ć À 'in particular Latin 1 for English and the languages of Western Europe. The IBM System/360 introduced an 8 -bit byte bankable byte memory, unlike the target or decimal memory, although its generic registers were 32 bits wide and the addresses were contained in the 24 lower bits of such addresses. Different System/360 models had several internal data routes; IBM System/360 Model 30 (1965) implements the System/360 to 32 -bit architecture, but had a width of 8 -bit native path and performed 8 bits 32 -bit arithmetic at a time [1]. The first 8 -bit microprocessor widely adopted was the Intel 8080, used in many hobbyist computers of the late seventies and early eighties, often with CP/M operating system; He had 8 -bit databases and 16 -bit addresses. The Zilog Z80 (compatible with the 8080) and the Motorola 6800 were also used on similar computers. The Z80 and Mos Technology 6502 8 -bit CPUs were widely used on home computers and second and third generation consoles of the seventies and eighties. Many CPUs or 8 -bit microcontrollers are the basis of current integrated systems. Details a 8 -bit register can store 28 different values. The interval of whole values that can be stored in 8 bit depends on the representation of whole numbers used. With the two most common representations, the interval is between 0 and 255 (28 à ĉ â- '1) for the representation as a binary number (not signed), and between à ĉ â-128 (â ĉ À'1 À'1 — À À '27) for the representation as a complement of two. 8 -bit CPUs use a 8-bit data, and can access 8 bits of data in one statement. The address bus À is typically a double octet (16 bits) width, due to and economical considerations. This implies a direct address space of 64 KB (65,536 bytes) on most 8-bit processors. Most home computers from the 8-bit era fully exploited the address space, such as the BBC Micro (Model B) with 32 KB of RAM plus 32 KB of ROM. Others like the very popular Commodore 64 had full 64 KB RAM, plus 20 KB ROM, meaning with 16-bit addressing you could not use all of the RAM by default (e.g. from the included BASIC language interpreter in ROM);[2] without exploiting bank switching, which allows for breaking the 64 KB (RAM) limit in some systems. Other computers would have as low as 1 KB (plus 4 KB ROM), such as the Spectrum ZX80 (while the later very popular Sinclair ZX Spectrum had more memory), or even only 128 bytes of RAM (plus storage from a ROM cartridge), as in an early game console Atari 2600 and thus 8-bit addressing would have been enough for the RAM, if it wouldn't have needed to cover ROM too). The Commodore 128, and other 8-bit systems, meaning still with 16-bit addressing, could use more than 64 KB, i.e. 128 KB RAM, also the BBC Master with it expandable to 512 KB of RAM. Further information: Zero page While in general 8-bit CPUs have 16-bit addressing, in some architectures you have both, such as in the MOS Technology 6502 CPU, where the zero page is used extensively, saving one byte in the instructions accessing that page, and also having 16-bit addressing instructions that take 2 bytes for the address plus 1 for the opcode. Commonly index registers are 8-bit (while other "8-bit" CPUs, such as Motorola 6800 had 16-bit index registers), such as the 6502 CPU, and then the size of the arrays addressed using indexed addressing instructions are at most 256 bytes, without needing longer code, i.e. meaning 8-bit addressing to each individual array. Notable 8-bit CPUs Main article: Microprocessor chronology The first commercial 8-bit processor was the Intel 8008 (1972) which was originally intended for the Datapoint 2200 intelligent Most of Intel's competitors started with such character-oriented 8-bit microprocessors. Modernized variants of these 8-bit machines are still one of the most common types of processors in embedded systems. Another notable 8-bit CPU is MOS Technology 6502. The system, and its variants, were used in a number of personal computers, such as Apple I and Apple II, the 8-bit Atari family, BBC Micro, Commodore PET, and Commodore VIC-20, and in a number of game consoles, such as the Atari 2600 and Nintendo Entertainment System. Intel 8008 1972 Datapoint 2200 Signetics 2650 1973 Intel 8080 1974 8008 compatible with source Motorola 6800 1974 Fairchild F8 1975 MOS 6502 1975 Similar to 6800, but incompatible Microchip PIC 1975 microcontroller electronic vard architecture 9002 1976 8-bit data, 12-bit addressing RCA 1802 1976 Zilog Z80 1976 8080 compatible binary Intel 8085 1977 8080 binary Zilog Z8 1978 Harvard architecture microcontroller Motorola 6809 1978 6800 compatible Intel 8 1 microcontroller Harvard architecture 19 80 Motorola 68008 1982 32-bit registers, 20- or 22-bit addressing, three 16-bit ALUs, 8-bit data bus; Motorola 68000 software-compatible, MGS 6809 hardware-compatible 1982 Enhanced 6502 custom-made for use in the Commodore 64 Ricoh 2A03 1982 BCD instructions for the Zilog Z180 19 Nintendo Entertainment System 85 Z80 compatible binary Motorola 68HC11 1985 Hudson HuC6280 1987 65C02 compatible binary Atmel AVR 1996 Zilog EZ80 1999 Z80 compatible binary Infineon XC800 2005 ale 68HC08 Motorola 6803 NEC 78K0(3) Use for 8-bit processors continue to be designed today for general instruction in computer hardware, as well as for the interests of hobbyists. One of these CPUs was designed and implemented using circuits 7400 series up "6019533901=didlo&gnitupmoc\_tib-8=elti?php.xedni/wgro.aidepikw.ne//spth" "6019533901=didlo&gnitupmoc\_tib-8=elti?php.xedni/wgro.aidepikw.ne//spth" morf deveirteR cigol CH47 gnisu htarcS morf edam dna scihparg htiw retupmoC + UPC tib-8 tliubemoH .oluap .onitnatsnoC ^ .1202 .4 rebmevoN deveirteR .draobrehtoM ."htarcS morf UPC tib-8 na tliuB dna dengiseD yuG siht" .)9102 .9 vraurbeF( leinaD ,suahreB O ^ .01-20-9002 deveirteR .82 1-8002 in lanigiro eht morf devihcrA .CEN ."0K87 CEN" ^ .80-40-1202 deveirteR .moc.kiw-46c.www ."ikiW-46C - gnihctiwS knaB" ^ .01-80-7102 in lanigiro eht morf )FDP( devihcrA .7800.28.dr/7411.01:iod .101ÀÀÀ 78 :)2( 8 .tnempoleveD dna hcræeseR fo lanruoj MBI .)FDP( "063/metsyS MBI eht fo erutctihcrA" ,)4691( .P .F .skoorB ;.A .G .wuaalB ;.M .G ,lhadaM ^ secnerefeR 1-kabneK osla eeS .esoprup siht rof desu era s'AGPF .stsiybboh na ,sreenigne ,stneduts gnireenigne rof esicrexe gniniart nommoc a si sreilmessa evtcepser rieht dna s'UPC tib-8 gningiseD

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